

ABSTRACT OF THE DISCLOSURE

An image display apparatus includes a plurality of scanning wires in an image display region for transmitting a scanning signal, a plurality of signal wires intersecting the plurality of scanning wires in the image display region for transmitting a signal voltage, a plurality of current driven electro-optical display elements each arranged in a pixel region surrounded by the wires connected to a common power supply, a plurality of driving elements in the pixel region connected with the electro-optical display elements and a plurality of memory control circuits for holding the signal voltage in response to the scanning signal to control driving of the driving elements based on the held signal voltage. The memory control circuit samples and holds the signal voltage while blocking a bias voltage from being applied to the driving elements, and subsequently applies the driving elements with the held signal voltage as the bias voltage.